

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/691,080

Confirmation No.: 9280

Applicants:

Barry E. Burke et al.

Filed:

October 22, 2003

TC/A.U.:

2818

Examiner:

C. Luu

Docket No.:

MIT8431L

For:

High-Yield, Single-Level Gate Charge-Coupled

Device Design and Fabrication

BOX AMENDMENT
COMMISSIONER FOR PATENTS
P. O. BOX 1450
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I hereby certify that this correspondence is being deposited on the date shown below with the United States Postal Service with sufficient postage as first class mail, under 37 CFR 1.8(a), in an envelope addressed to: Box Amendment, Commissioner For Patents PO Box 1450, Alexandria, VA 22313-1450

RESPONSE

This is in response to the Examiner's Action mailed March 10, 2005. A petition for a two -month extension of time accompanies this response.

Please amend the above-identified application as follows:

Amendments to the Specification begin on Page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.